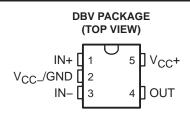
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- Wide Range of Supply Voltages, Single Supply 3 V to 30 V, or Dual Supplies
- Class AB Output Stage
- True Differential-Input Stage
- Low Input Bias Current
- Internal Frequency Compensation
- Short-Circuit Protection



# description/ordering information

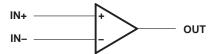
The TL343 is a single operational amplifier similar in performance to the  $\mu$ A741, but with several distinct advantages. It is designed to operate from a single supply over a range of voltages from 3 V to 30 V. Operation from split supplies also is possible, provided the difference between the two supplies is 3 V to 30 V. The common-mode input range includes the negative supply. Output range is from the negative supply to  $V_{CC} - 1.5$  V.

#### ORDERING INFORMATION

TA	V <sub>IO</sub> MAX AT 25°C	PACKAG	<sub>GE</sub> †	ORDERABLE PART NUMBER	TOP-SIDE MARKING‡
4000 1- 40500	40>/	00T 00 F (DD) ()	Reel of 3000	TL343IDBVR	T41
-40°C to 125°C	10 mV	SOT-23-5 (DBV)	Reel of 250	TL343IDBVT	T4I_

<sup>†</sup> Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

#### symbol



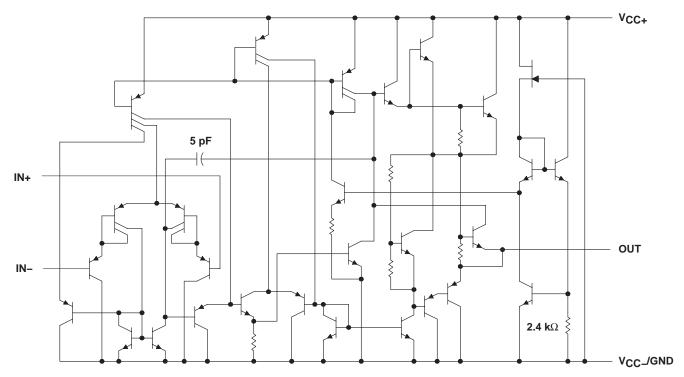


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<sup>&</sup>lt;sup>‡</sup>The actual top-side marking has one additional character that designates the assembly/test site.

### schematic



NOTE A: Component values shown are nominal.

# absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

		MAX	UNIT
Complexed to a Constitute (A)	CC+	18	.,
Supply voltage (see Note 1)	CC-	-18	V
Supply voltage, V <sub>CC+</sub> with respect to V <sub>CC-</sub>	36	V	
Differential input voltage (see Note 2)	±36	V	
Input voltage (see Notes 1 and 3)		±18	V
Package thermal impedance, θ <sub>JA</sub> (see Notes 4 and 5)		206	°C/W
Operating virtual junction temperature, T <sub>J</sub>		150	°C
Storage temperature range, T <sub>stg</sub>	-65 to 150	°C	

- NOTES: 1. These voltage values are with respect to the midpoint between V<sub>CC+</sub> and V<sub>CC-</sub>.
  - 2. Differential voltages are at IN+ with respect to IN-.

  - Neither input must ever be more positive than V<sub>CC+</sub> or more negative than V<sub>CC-</sub>.
    Maximum power dissipation is a function of T<sub>J</sub>(max), θ<sub>JA</sub>, and T<sub>A</sub>. The maximum allowable power dissipation at any allowable ambient temperature is P<sub>D</sub> = (T<sub>J</sub>(max) T<sub>A</sub>)/θ<sub>JA</sub>. Selecting the maximum of 150°C can affect reliability.
  - 5. The package thermal impedance is calculated in accordance with JESD 51-7.



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# recommended operating conditions

		MIN	MAX	UNIT
VCC	Single-supply voltage	3	30	V
V <sub>CC+</sub>	Dead and the second sec	1.5	15	.,
VCC-	Dual-supply voltage	-1.5	-15	V
TA	Operating free-air temperature	-40	125	°C

# electrical characteristics at specified free-air temperature, $V_{CC\pm}$ = ±15 V (unless otherwise noted)

	PARAMETER	TE	TEST CONDITIONS <sup>†</sup>			TYP	MAX	UNIT
.,	land effect with an	On a Nata O		25°C		2	10	>/
VIO	Input offset voltage	See Note 6	See Note 6				12	mV
$\alpha_{V_{IO}}$	Temperature coefficient of input offset voltage	See Note 6		Full range		10		μV/°C
1	land offers comment	Con Note C		25°C		30	50	A
lio	Input offset current	See Note 6		Full range			200	nA
α <sub>IIO</sub>	Temperature coefficient of input offset current	See Note 6		Full range		50		pA/°C
1	land bing compat	Con Note C		25°C		-200	-500	A
l <sub>IB</sub>	Input bias current	See Note 6	See Note 6				-800	nA
VICR	Common-mode input voltage range <sup>‡</sup>			25°C	V <sub>CC</sub> - to 13	V <sub>CC</sub> - to 13.5		V
		R <sub>L</sub> = 10 kΩ		25°C	±12	±13.5		
Vом	Peak output-voltage swing	R <sub>L</sub> = 2 kΩ		25°C	±10	±13		V
				Full range	±10			
Δ	Large-signal differential	V- 140 V	D. 010	25°C	20	200		V/mV
AVD	voltage amplification	$V_0 = \pm 10 \text{ V},$	$R_L = 2 k\Omega$	Full range	15			V/mV
ВОМ	Maximum-output-swing bandwidth	V <sub>OPP</sub> = 20 V, THD ≤ 5%,	$A_{VD} = 1$ , $R_L = 2 k\Omega$	25°C		9		kHz
B <sub>1</sub>	Unity-gain bandwidth	$V_O = 50 \text{ mV},$	$R_L = 10 \text{ k}\Omega$	25°C		1		MHz
φm	Phase margin	C <sub>L</sub> = 200 pF,	$R_L = 2 k\Omega$	25°C		44		Deg
rį	Input resistance	f = 20 Hz		25°C	0.3	1		$M\Omega$
r <sub>O</sub>	Output resistance	f = 20 Hz		25°C		75		Ω
CMRR	Common-mode rejection ratio	V <sub>IC</sub> = V <sub>ICR</sub> (min	n)	25°C	70	90		dB
kSVS	Supply-voltage sensitivity ( $\Delta V_{IO}/\Delta V_{CC}$ )	$V_{CC\pm} = \pm 2.5 \text{ to}$	±15 V	25°C		30	150	μV/V
los	Short-circuit output current§			25°C	±10	±30	±55	mA
Icc	Total supply current	No load,	See Note 6	25°C		0.7	2.8	mA

<sup>†</sup> All characteristics are measured under open-loop conditions, with zero common-mode voltage, unless otherwise specified. Full range for T<sub>A</sub> is -40°C to 125°C.

NOTE 6:  $V_{IO}$ ,  $I_{IO}$ ,  $I_{IB}$ , and  $I_{CC}$  are defined at  $V_{O} = 0$ .



 $<sup>\</sup>ddagger$  The V<sub>ICR</sub> limits are linked directly, volt-for-volt, to supply voltage; the positive limit is 2 V less than V<sub>CC+</sub>.

<sup>§</sup> Temperature and/or supply voltages must be limited to ensure that the dissipation rating is not exceeded.

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# electrical characteristics, $V_{CC+}$ = 3 V and 5 V, $V_{CC-}$ = 0 V, $T_A$ = 25°C (unless otherwise noted)

	PARAMETER	TEST CONDITIONS†	MIN	TYP	MAX	UNIT
VIO	Input offset voltage	V <sub>O</sub> = 1.5 V and 2.5 V		2	10	mV
IIO	Input offset current	V <sub>O</sub> = 1.5 V and 2.5 V		30	50	nA
I <sub>IB</sub>	Input bias current	V <sub>O</sub> = 1.5 V and 2.5 V		-200	-500	nA
VOM	Peak output voltage swing‡	$R_L = 10 \text{ k}\Omega$	3.3	3.5		V
AVD	Large-signal differential voltage amplification	$V_O = 1.7 \text{ V to } 3.3 \text{ V}, \qquad R_L = 2 \text{ k}\Omega$	20	200		V/mV
ksvs	Supply-voltage sensitivity ( $\Delta V_{IO}/\Delta V_{CC\pm}$ )	$V_{CC\pm} = \pm 2.5 \text{ V to } \pm 15 \text{ V}$			150	μV/V
ICC	Supply current	V <sub>O</sub> = 1.5 V and 2.5 V, No load		0.7	1.75	mA

TAll characteristics are measured under open-loop conditions, with zero common-mode input voltage, unless otherwise specified.

# operating characteristics, $V_{CC\pm}$ = $\pm 15$ V, $T_A$ = 25°C, $A_{VD}$ = 1 (unless otherwise noted)

	PARAMETER		TYP	UNIT			
SR	Slew rate at unity gain	$V_{I} = \pm 10 \text{ V},$	C <sub>L</sub> = 100 pF,	$R_L = 2 k\Omega$ ,	See Figure 1	1	V/μs
t <sub>r</sub>	Rise time	$\Delta V_{O} = 50 \text{ mV},$	$C_{L} = 100 pF$ ,	$R_L = 10 \text{ k}\Omega$ ,	See Figure 1	0.35	μs
t <sub>f</sub>	Fall time	$\Delta V_{O} = 50 \text{ mV},$	$C_{L} = 100 pF$ ,	$R_L = 10 \text{ k}\Omega$ ,	See Figure 1	0.35	μs
	Overshoot factor	$\Delta V_O = 50 \text{ mV},$	C <sub>L</sub> = 100 pF,	$R_L = 10 \text{ k}\Omega$ ,	See Figure 1	20%	
	Crossover distortion	$V_{I(PP)} = 30 \text{ mV},$	V <sub>OPP</sub> = 2 V,	f = 10 kHz		1%	

### PARAMETER MEASUREMENT INFORMATION

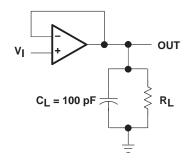
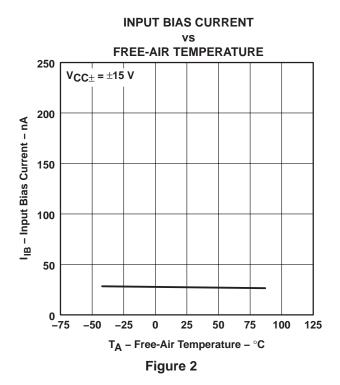
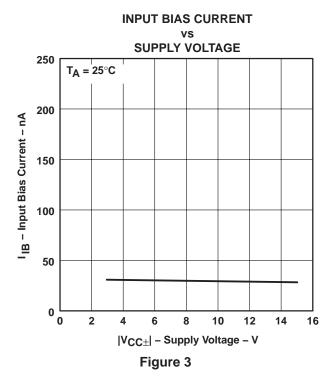


Figure 1. Unity-Gain Amplifier

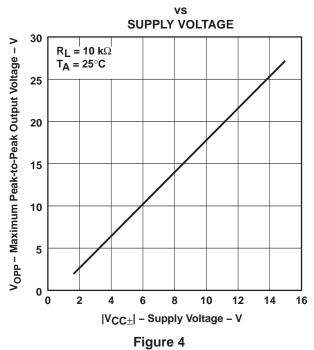
<sup>‡</sup> Output swings essentially to ground.

# TYPICAL CHARACTERISTICS<sup>†</sup>

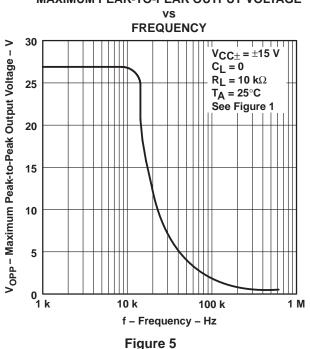




#### **MAXIMUM PEAK-TO-PEAK OUTPUT VOLTAGE**



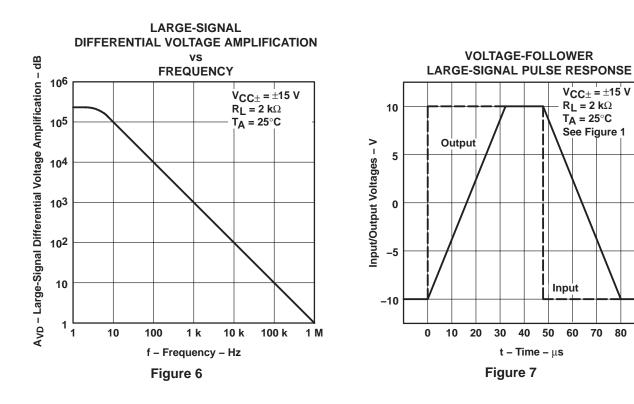
# MAXIMUM PEAK-TO-PEAK OUTPUT VOLTAGE



<sup>†</sup> Operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied.



# TYPICAL CHARACTERISTICS<sup>†</sup>



80

<sup>†</sup> Operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied.







i.com 29-May-2007

#### **PACKAGING INFORMATION**

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
TL343IDBVR	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL343IDBVRE4	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL343IDBVRG4	ACTIVE	SOT-23	DBV	5	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL343IDBVT	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL343IDBVTE4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
TL343IDBVTG4	ACTIVE	SOT-23	DBV	5	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

**Pb-Free** (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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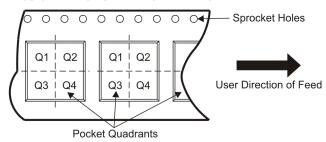
# TAPE AND REEL INFORMATION





A0	Dimension designed to accommodate the component width
	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



### \*All dimensions are nominal

Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TL343IDBVR	SOT-23	DBV	5	3000	180.0	9.2	3.23	3.17	1.37	4.0	8.0	Q3
TL343IDBVR	SOT-23	DBV	5	3000	178.0	9.0	3.23	3.17	1.37	4.0	8.0	Q3
TL343IDBVT	SOT-23	DBV	5	250	180.0	9.2	3.23	3.17	1.37	4.0	8.0	Q3





\*All dimensions are nominal

1								
	Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
	TL343IDBVR	SOT-23	DBV	5	3000	205.0	200.0	33.0
	TL343IDBVR	SOT-23	DBV	5	3000	565.0	140.0	75.0
	TL343IDBVT	SOT-23	DBV	5	250	205.0	200.0	33.0

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